

Application No. 09/986,262

- 2 -

October 26, 2004

IN THE DRAWINGS

The Official Draftsperson objected to Figures 3 to 5 on the basis that the lines, numbers and letters were of poor quality and not legible. Proposed formal drawings containing Figures 1 to 5 are enclosed for the Examiner's approval. No new matter has been added to the drawings.

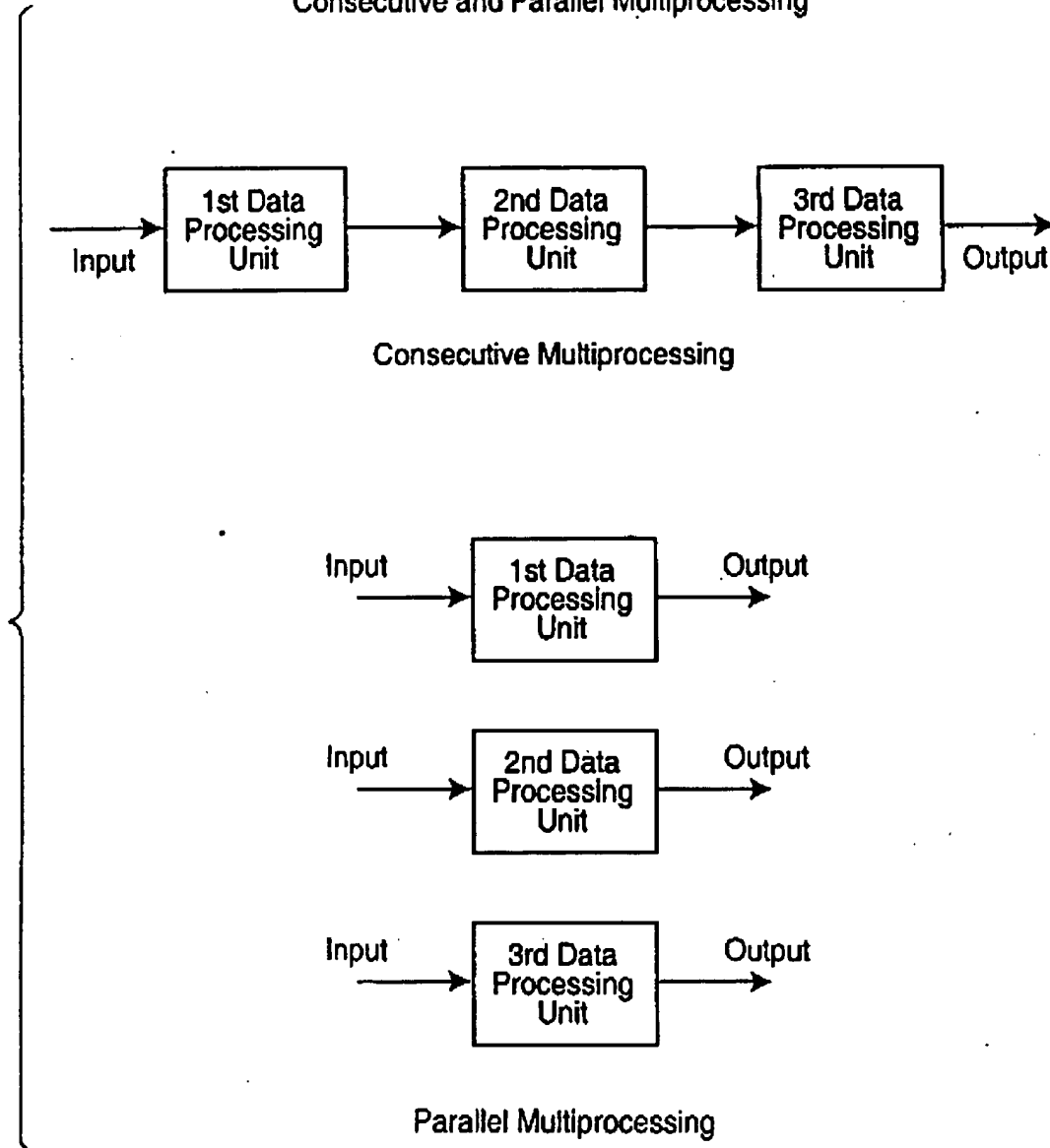
Figure 1**Consecutive and Parallel Multiprocessing**

Figure 2

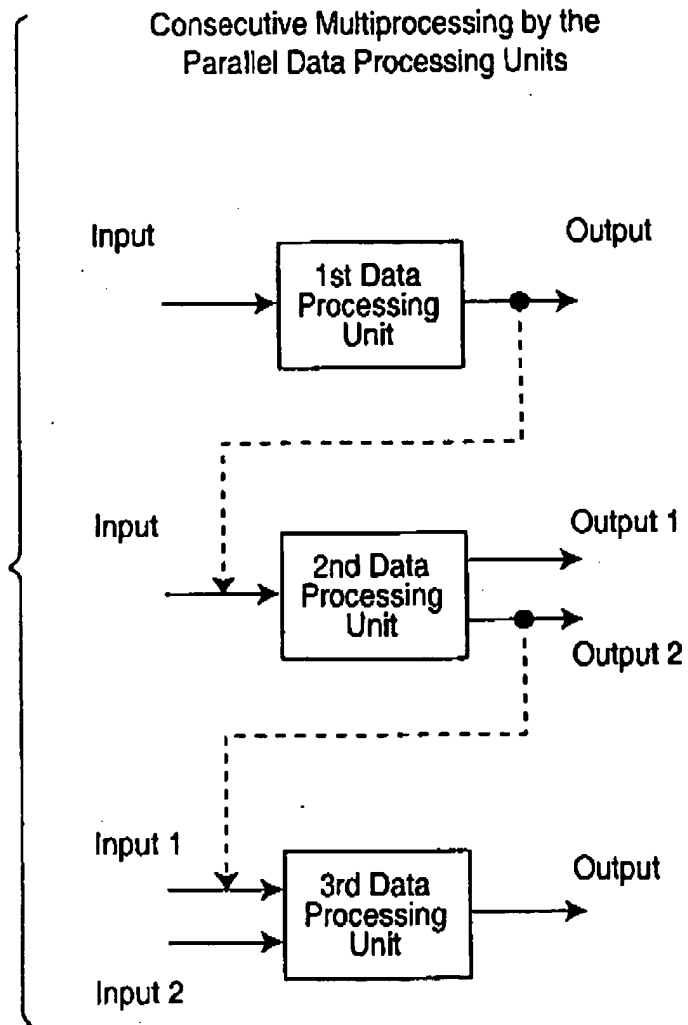


Figure 3

Example of System Organization of a Processor
with Multiple Data Processing Units

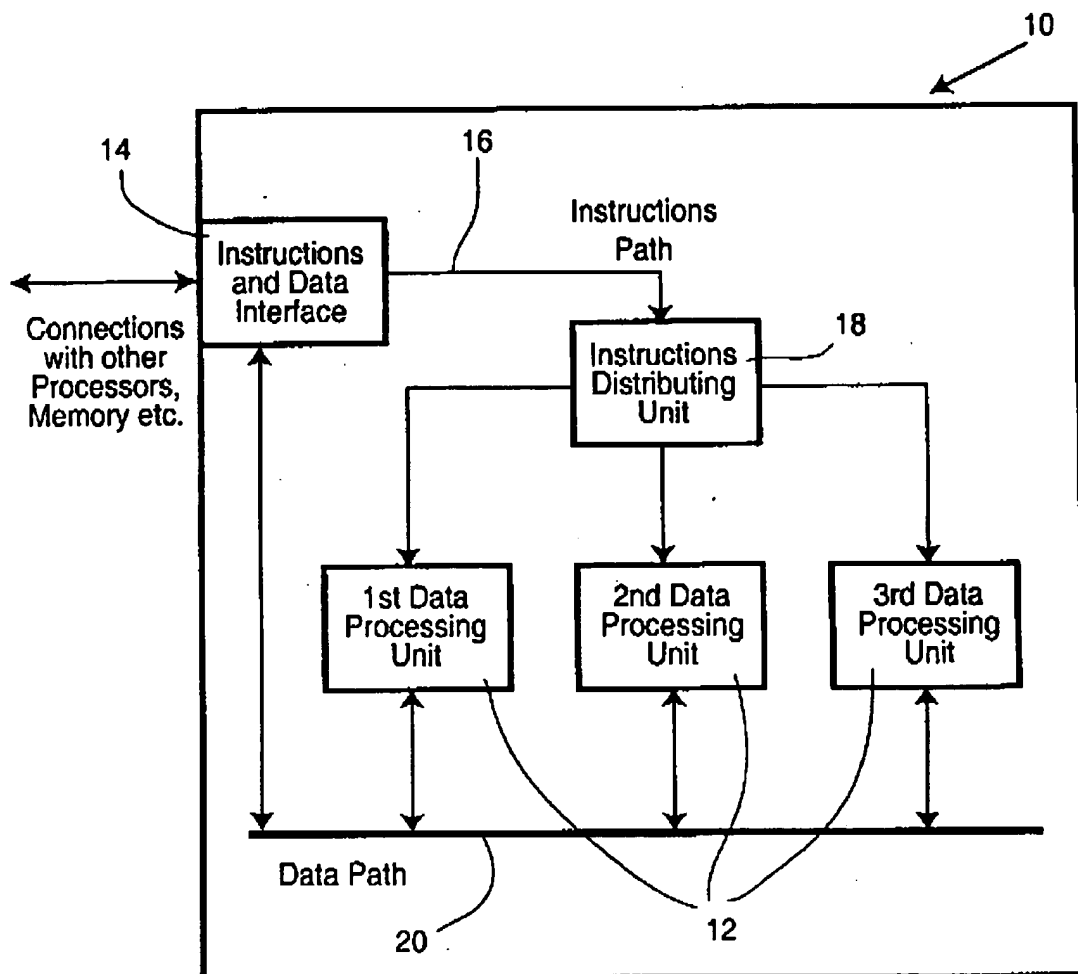


Figure 4

Top-Level Block Diagram of
Data Processing Unit

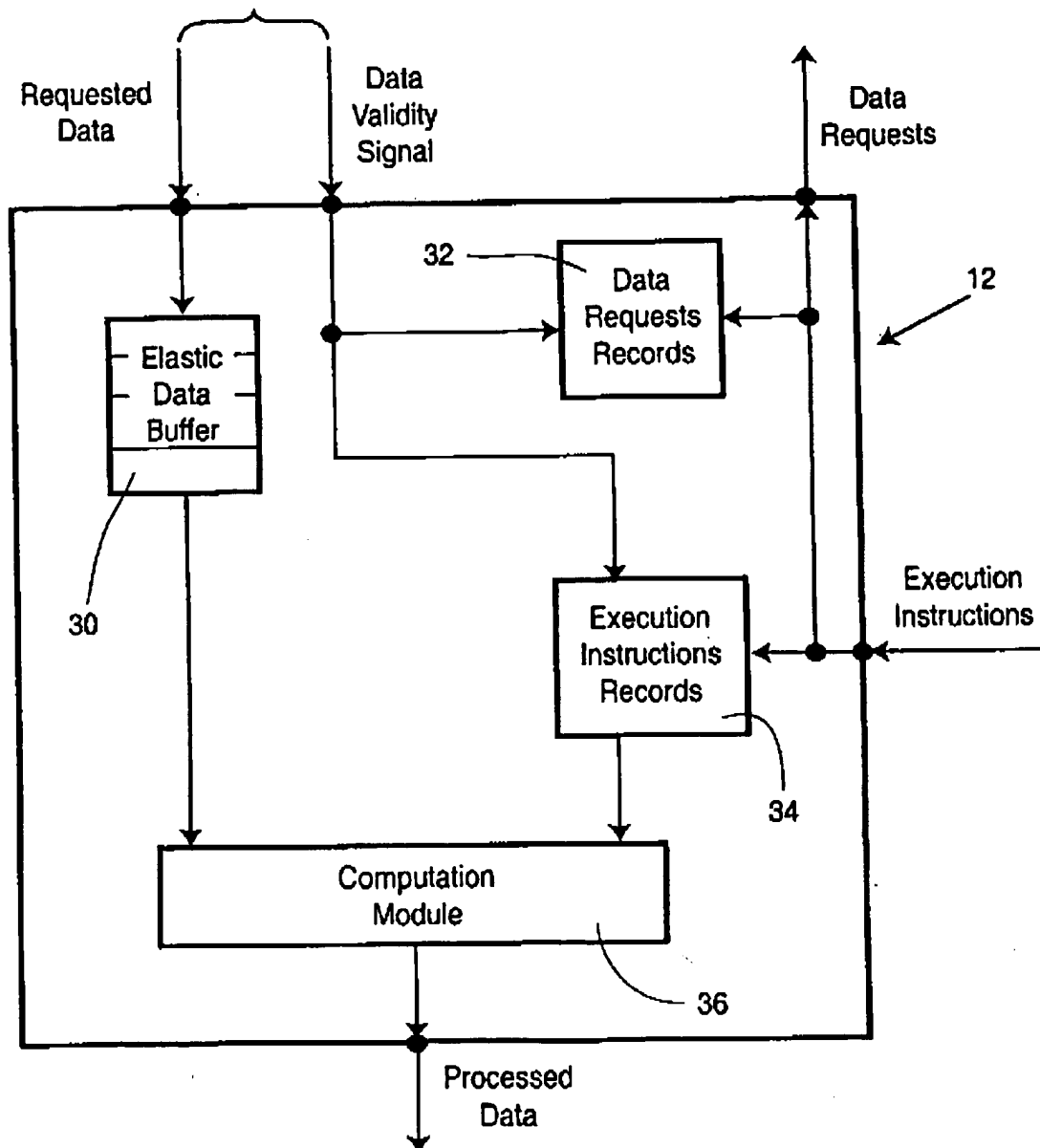


Figure 5

Schematic Work of an Elastic Buffer

